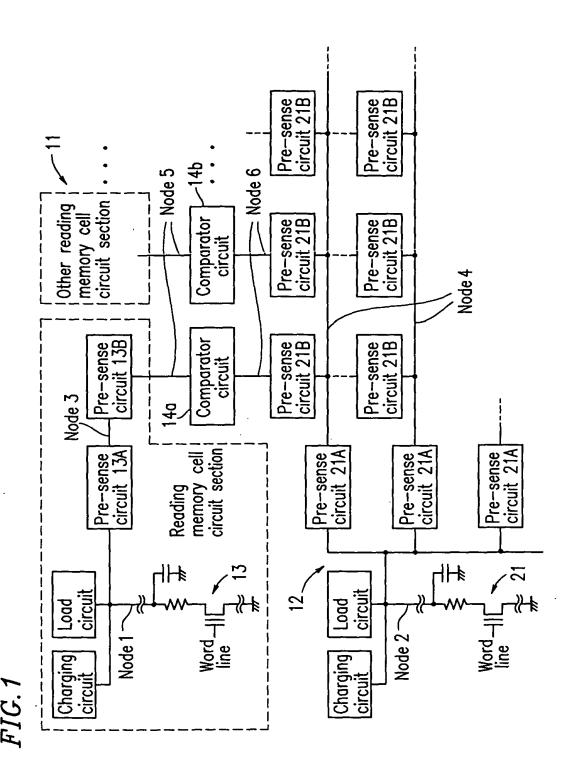
Inventor: Yoshin Application No.: To Be Assigned

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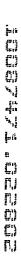
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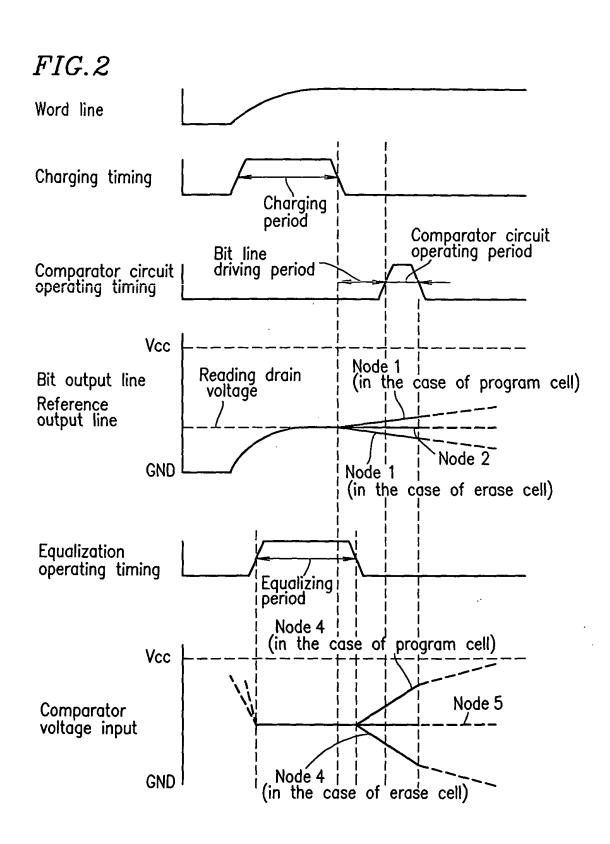


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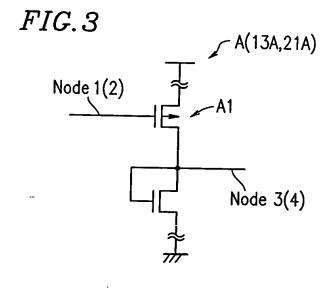


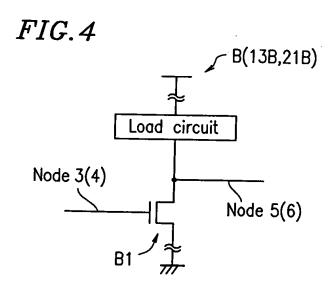


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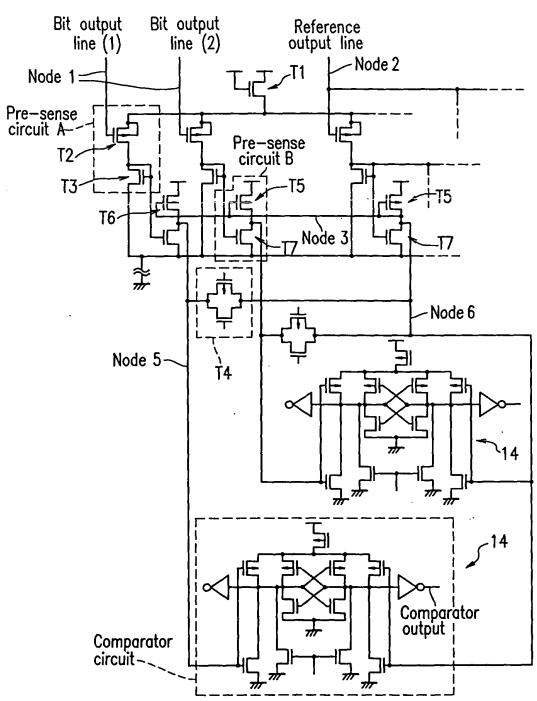
ventor: Yoshinao MORIKAWA Application No.: To Be Assigned

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FIG.5



Title: REFERENCE VOLTAGE GENERATION CIRCUIT FOR

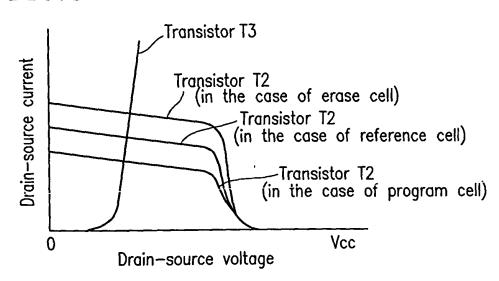
SEMICOND OR MEMORY DEVICE, Inventor: Yos MORIKAWA

Inventor: Yos A MORIKAWA Application No.: To Be Assigned Docket No.: 299002054200

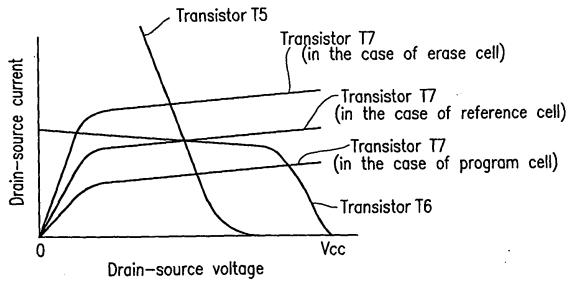
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FIG. 6







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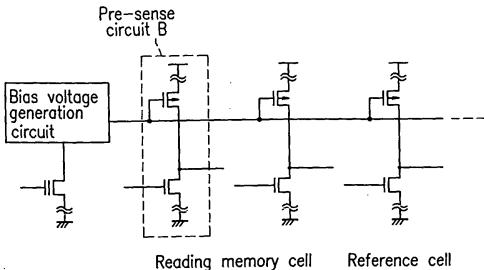
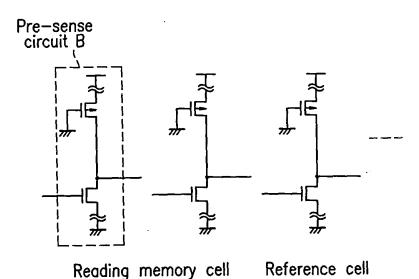


FIG.9



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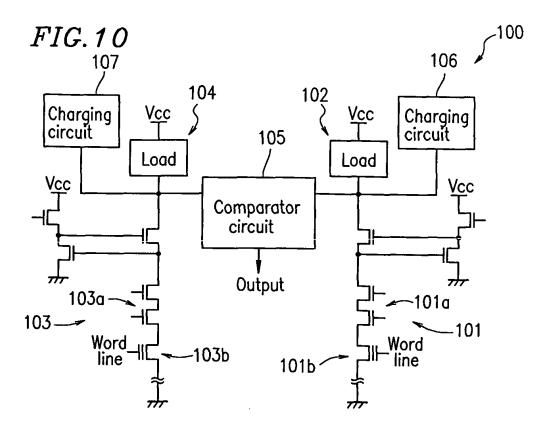
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Inventor: Yoshin ORIKAWA Application No.: To Be Assigned Docket No.: 299002054200

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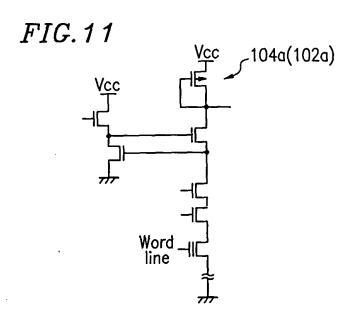
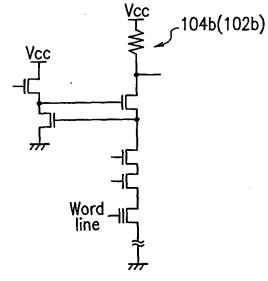


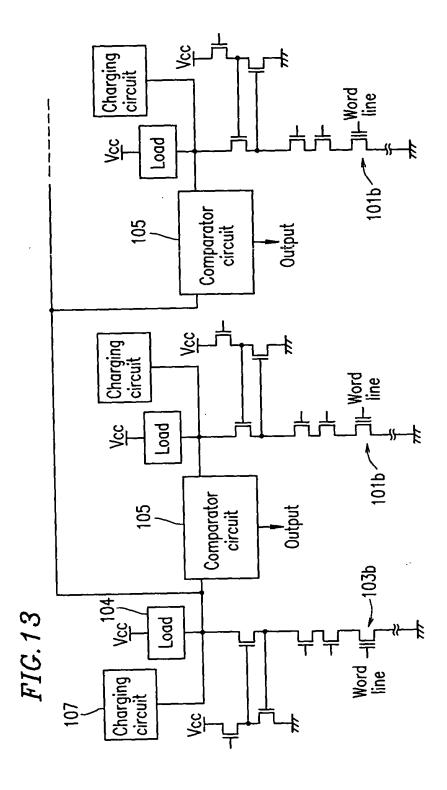
FIG. 12



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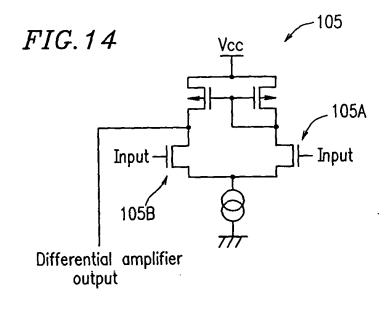


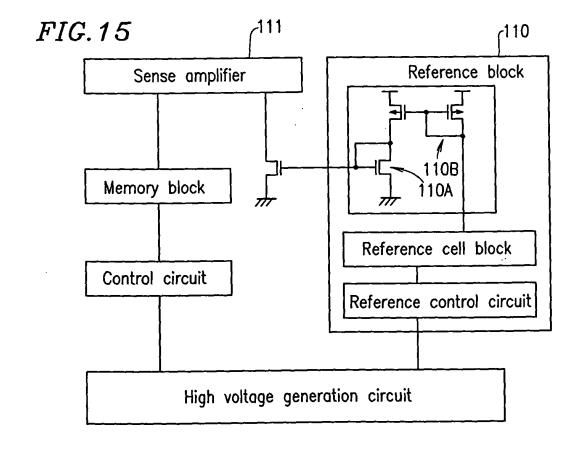
Title: REFERENCE VOLTAGE GENERATION CIRCUIT FOR UCTOR MEMORY DEVICE,

Invento. hinao MORIKAWA Application No.: To Be Assigned Docket No.: 299002054200

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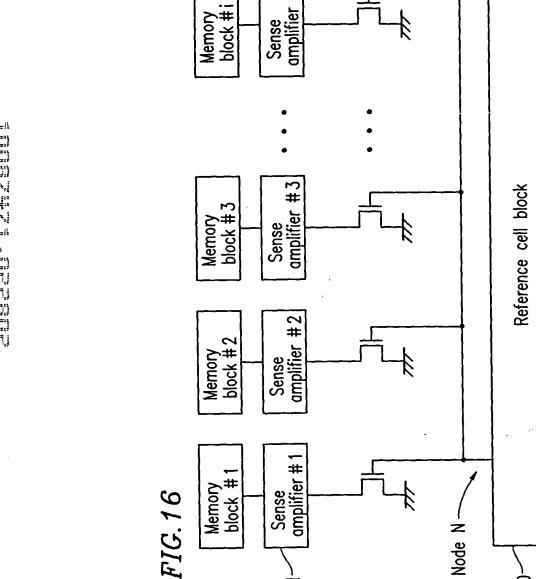
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CTOR MEMORY DEVICE, nao MORIKAWA

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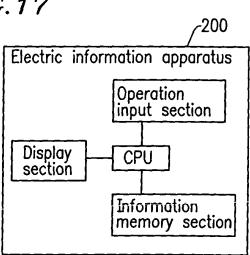
Inventor: Inao MORIKAWA
Application No.: To Be Assigned

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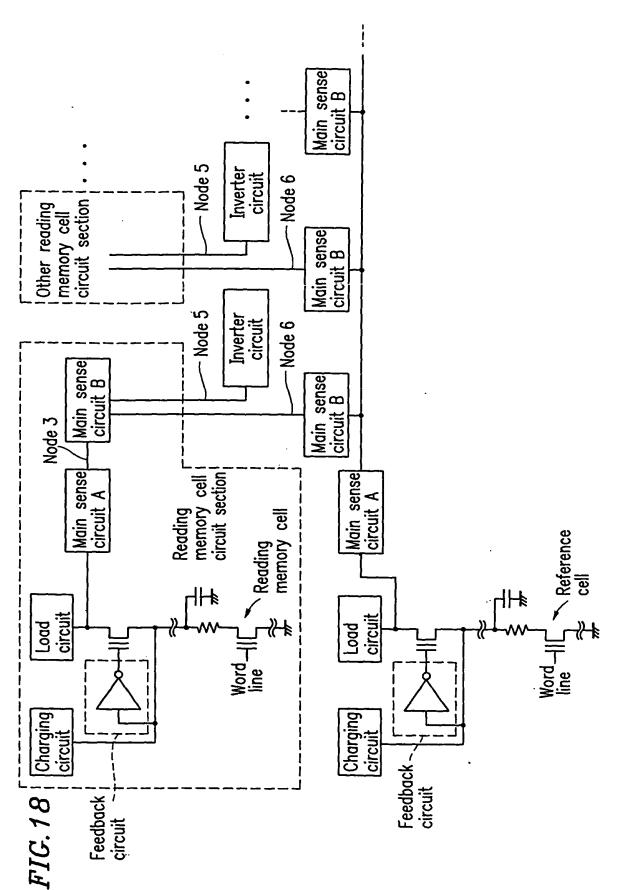




Inventor: nao MORIKAWA Application No.: To Be Assigned Docket No.: 299002054200

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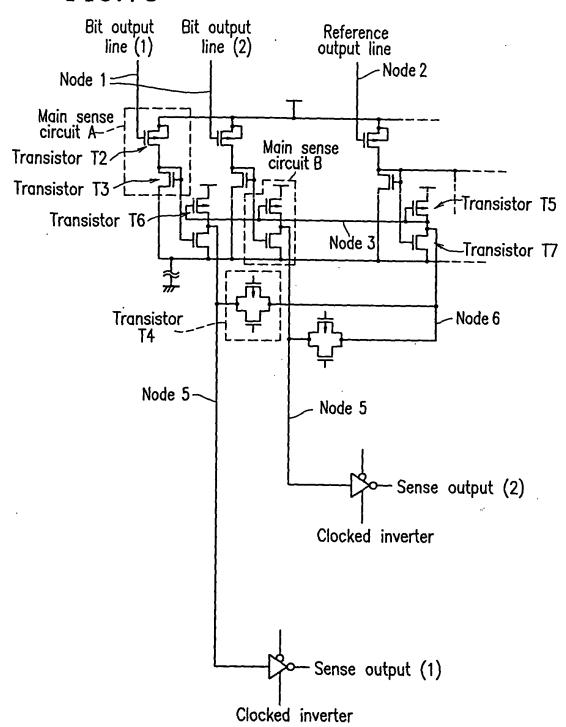
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FIG. 19



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